

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A data transfer device that transfers data, via a plurality of signal lines, comprising:

a data transfer timing signal output device that outputs a random timing signal for the data transfer, by changing a frequency of the timing signal randomly for each of the plurality of signal lines without becoming a specific cycle, so as to delimit an energy density in the plurality of signal lines.

2. (Original) The data transfer device according to claim 1, wherein the data transfer timing signal output device randomly changes at least one of a data transfer start timing and a data transfer end timing.

3. (Original) The data transfer device according to claim 1, wherein a flat harness connects the data transfer device to another device to which the data is transferred.

4. (Currently Amended) A data transfer device that transfers data using a system clock, comprising:

a transfer clock generating device that generates a transfer clock that determines a timing for the data transfer, based on the system clock; and

a transfer clock changing device that randomly changes a frequency of the transfer clock generated by the transfer clock generating device, to randomly change the transfer of data through at least one signal line without becoming a specific cycle, so as to delimit an energy density in the at least one signal line.

5. (Original) The data transfer device according to claim 4, wherein the transfer clock changing device randomly changes at least one of a timing for the transfer clock to

make a transition to a high level and a timing for the transfer clock to make a transition to a low level.

6. (Currently Amended) The data transfer device according to claim 4, wherein the at least one signal line is a flat harness that connects the data transfer device to another device to which the data is transferred.

7. (Currently Amended) A data transfer device that transfers data using a system clock, comprising:

a transfer clock generating device that generates a transfer clock that determines a timing for the data transfer, based on the system clock;

a delayed transfer clock generating device that shifts the transfer clock generated by the transfer clock generating device by a predetermined amount, to generate a plurality of delayed transfer clocks; and

a delayed transfer clock selecting device that randomly selects one of the delayed transfer clocks generated by the delayed transfer clock generating device to randomly change the transfer of data through at least one signal line, without becoming a specific cycle, so as to delimit an energy density in the at least one signal line,;

wherein the data transfer device transfers the data in accordance with the delayed transfer clock selected by the delayed transfer clock selecting device.

8. (Currently Amended) The data transfer device according to claim 7, wherein the at least one signal line is a flat harness that connects the data transfer device to another device to which the data is transferred.

9. (Currently Amended) A printing apparatus including a data transfer device that transfers print data to a print head, via a plurality of signal lines, comprising:

a data transfer timing signal output device that outputs a random timing signal for the data transfer, by changing a frequency of the timing signal randomly for each of the

plurality of signal lines without becoming a specific cycle, so as to delimit an energy density in the plurality of signal lines.

10. (Original) The printing apparatus according to claim 9, wherein the data transfer timing signal output device randomly changes at least one of a data transfer start timing and a data transfer end timing.

11. (Original) The printing apparatus according to claim 9, wherein a flat harness connects the data transfer device to the print head.

12. (Currently Amended) A printing apparatus including a data transfer device that transfers print data to a print head, using a system clock, comprising:

a transfer clock generating device that generates a transfer clock that determines a timing for print data transfer, based on the system clock; and

a transfer clock changing device that randomly changes a frequency of the transfer clock generated by the transfer clock generating device to randomly change the transfer of data through at least one signal line without becoming a specific cycle, so as to delimit an energy density in the at least one signal line.;

wherein the data transfer device transfers the print data to the print head, based on the transfer clock whose frequency is randomly changed by the transfer clock changing device.

13. (Original) The printing apparatus according to claim 12, wherein the transfer clock changing device randomly changes at least one of a timing for the transfer clock to make a transition to a high level and a timing for the transfer clock to make a transition to a low level.

14. (Original) The printing apparatus according to claim 12, wherein a flat harness connects the data transfer device to the print head.

15. (Currently Amended) A printing apparatus including a data transfer device that transfers print data to a print head, using a system clock, comprising:

a transfer clock generating device that generates a transfer clock that determines a timing for the data transfer, based on the system clock;

a delayed transfer clock generating device that shifts the transfer clock generated by the transfer clock generating device by a predetermined amount, to generate a plurality of delayed transfer clocks; and

a delayed transfer clock selecting device that randomly selects one of the delayed transfer clocks generated by the delayed transfer clock generating device; to randomly change the transfer of data through at least one signal line without becoming a specific cycle, so as to delimit an energy density in the at least one signal line,

—wherein the data transfer device transfers the print data to the print head in accordance with the delayed transfer clock selected by the delayed transfer clock selecting device.

16. (Original) The printing apparatus according to claim 15, wherein a flat harness connects the data transfer device to the print head.

17. (Currently Amended) A method of transferring reducing radiant noises in at least one signal line during the transfer of data, comprising:

generating a transfer clock that determines a timing for the data transfer;

shifting the transfer clock by a predetermined amount to generate a plurality of delayed transfer clocks;

randomly selecting one of the delayed transfer clocks; and

transferring the data in accordance with the randomly selected delayed transfer clock without becoming a specific cycle, so as to delimit an energy density in the at least one signal line.